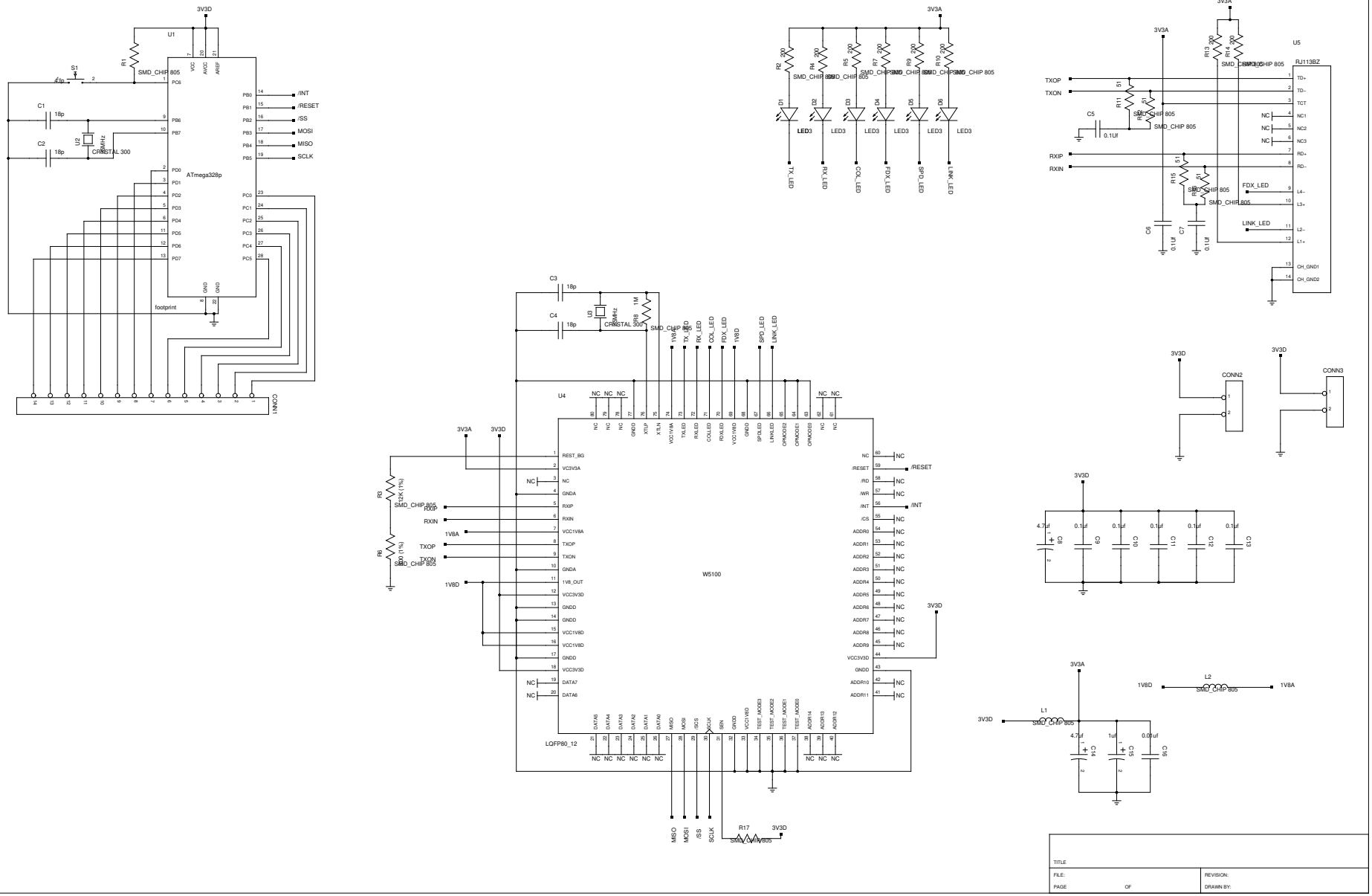
3V3D
U1
R1
SMD_CHIP 805
S1
4Tp
C1
18p
C2
18p
U2
CRYSTAL 300
ATmega328p
footprint
/INT
/RESET
/SS
MOSI
MISO
SCLK
CONN1
3V3A
R2
R4
R5
R7
R9
R10
200
D1
D2
D3
D4
D5
D6
LED3
TX_LED
RX_LED
COL_LED
FDX_LED
SPD_LED
LINK_LED
U5
RJ113BZ
TXOP
TXON
RXIP
RXIN
R11
R12
R13
R14
R15
R16
51
C5
0.1Uf
C6
C7
TD+
TD-
TCT
RD+
RD-
L4-
L3+
L2-
L1+
CH_GND1
CH_GND2
C3
C4
18p
U3
25MHz
R8
1M
U4
W5100
LQFP80_12
REST_BG
VC3V3A
RXIP
RXIN
TXOP
TXON
1V8_OUT
VCC3V3D
GNDD
GNDA
VCC1V8D
DATA7
DATA6
/RESET
/RD
/WR
/INT
/CS
ADDR0
ADDR1
ADDR2
ADDR3
ADDR4
ADDR5
ADDR6
ADDR7
ADDR8
ADDR9
ADDR10
ADDR11
R3
12K (1%)
R6
680 (1%)
1V8A
1V8D
MISO
MOSI
/SS
SCLK
R17
CONN2
CONN3
C8
4.7uf
C9
C10
C11
C12
C13
0.1uf
L1
L2
SMD_CHIP 805
C14
C15
C16
1uf
0.01uf
TITLE
FILE:
PAGE
OF
REVISION:
DRAWN BY: